

	Hits	Search Text	DBs
1	45	reduc\$3 with (processor adj array)	USPAT; US-PGPUB
2	10	reduc\$3 with (processor adj array) with size	USPAT; US-PGPUB
3	2538	re\$1configur\$5 with processor	USPAT; US-PGPUB
4	796	re\$1configur\$5 adj3 processor	USPAT; US-PGPUB
5	119	(re\$1configur\$5 adj3 processor) with dynamic\$5	USPAT; US-PGPUB
6	214	(re\$1configur\$5 adj3 processor) with operat\$5	USPAT; US-PGPUB
7	20	((re\$1configur\$5 adj3 processor) with dynamic\$5) and ((re\$1configur\$5 adj3 processor) with operat\$5)	USPAT; US-PGPUB
8	7	(re\$1configur\$5 near3 processor) with (operat\$5 near (during or while))	USPAT; US-PGPUB
9	238	reduc\$3 with (processor near2 size)	USPAT; US-PGPUB
10	3	(reduc\$3 with (processor near2 size)) same (re\$1configur\$5)	USPAT; US-PGPUB
11	3	re\$1configur\$5 with (processor adj array) with size	USPAT; US-PGPUB
12	1	de\$1featur\$5 with dynamic\$5	USPAT; US-PGPUB
13	1	de\$1featur\$5 with (cpu or processor or micro\$1processor)	USPAT; US-PGPUB
14	17	de\$1featur\$5	USPAT; US-PGPUB
15	203	(disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)	USPAT; US-PGPUB
16	25	((disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	USPAT; US-PGPUB
17	6	((disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)) and 713/100	USPAT; US-PGPUB
18	84	(disabl\$3 near2 (portion\$1 or sub\$1system)) with (processor or cpu or micro\$1processor)	USPAT; US-PGPUB
19	3	((disabl\$3 near2 (portion\$1 or sub\$1system)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	USPAT; US-PGPUB
20	3	((disabl\$3 near2 (portion\$1 or sub\$1system)) with (processor or cpu or micro\$1processor)) and 713/100	USPAT; US-PGPUB

	Hits	Search Text	DBs
21	68	(disabl\$3 near2 (element\$1)) with (processor or cpu or micro\$1processor)	USPAT; US-PGPUB
22	5	((disabl\$3 near2 (element\$1)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	USPAT; US-PGPUB
23	2	((disabl\$3 near2 (element\$1)) with (processor or cpu or micro\$1processor)) and 713/100	USPAT; US-PGPUB
24	1	(selective\$3 with (disabl\$3 near2 (processor or cpu or micro\$1processor))) and 713/100	USPAT; US-PGPUB
25	36	(disabl\$3 near2 (processor or cpu or micro\$1processor)) and 713/100	USPAT; US-PGPUB
26	544	(disabl\$3 near2 (processor or cpu or micro\$1processor)) same instruction\$1	USPAT; US-PGPUB
27	28	(disabl\$3 near2 (processor or cpu or micro\$1processor)) same (prevent\$3 with instruction\$1)	USPAT; US-PGPUB
28	54	(disabl\$3 near2 (processor or cpu or micro\$1processor)) same (complet\$3 with instruction\$1)	USPAT; US-PGPUB
29	0	(disabl\$3 near2 (processor or cpu or micro\$1processor)) same (complet\$3 with (pending or outstanding) with instruction\$1)	USPAT; US-PGPUB
30	0	(disabl\$3 near2 (module or element or function\$2)) same (complet\$3 with (pending or outstanding) with instruction\$1)	USPAT; US-PGPUB
31	227	((processor or cpu or micro\$1processor)) same (complet\$3 with (pending or outstanding) with instruction\$1)	USPAT; US-PGPUB
32	147	((processor or cpu or micro\$1processor)) with (complet\$3 with (pending or outstanding) with instruction\$1)	USPAT; US-PGPUB
33	30	((processor or cpu or micro\$1processor)) with (complet\$3 with (pending or outstanding) with instruction\$1)) and "713"	USPAT; US-PGPUB
34	1	processor with array with fenc\$3	USPAT; US-PGPUB
35	133	processor with fenc\$3	USPAT; US-PGPUB
36	11	(processor with fenc\$3) same (configur\$5 or re\$1configur\$5)	USPAT; US-PGPUB

	Hits	Search Text	DBs
37	4	reduc\$3 with (processor adj array)	EPO; JPO
38	0	de\$1featur\$5 with (cpu or processor or micro\$1processor)	EPO; JPO
39	0	((disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	EPO; JPO
40	8	(re\$1configur\$5 same (pending adj instructions))	USPAT; US-PGPUB
41	0	(re\$1configur\$5 same (pending adj operations))	USPAT; US-PGPUB
42	65	((re\$1configur\$5 with processor) same (pending))	USPAT; US-PGPUB
43	224	((re\$1configur\$5 with processor) same (complet\$3))	USPAT; US-PGPUB
44	14	((re\$1configur\$5 with processor) same ((complet\$3 or finish\$3) with (wait\$3 or until)))	USPAT; US-PGPUB
45	75	((re\$1configur\$5 with processor) same ((complet\$3 or finish\$3) with (instructions or operations or processes or tasks)))	USPAT; US-PGPUB
46	4	((re\$1configur\$5 with processor) same (outstanding))	USPAT; US-PGPUB
47	44	((re\$1configur\$5 with fpga) same ((complet\$3 or finish\$3) with (instructions or operations or processes or tasks)))	USPAT; US-PGPUB
48	15	((re\$1configur\$5 with fpga) same ((complet\$3 or finish\$3) with (wait\$3 or until)))	USPAT; US-PGPUB
49	1	(row with column with mask) and (re\$1configur\$5 adj processor)	USPAT; US-PGPUB
50	1	((row or column) with mask\$3) and (re\$1configur\$5 adj processor)	USPAT; US-PGPUB
51	5	(mask near2 register) and (re\$1configur\$5 adj processor)	USPAT; US-PGPUB
52	76	(mask near2 register) and (processor adj array)	USPAT; US-PGPUB
53	19	(row with column with mask) and (processor adj array)	USPAT; US-PGPUB
54	6	((mask near2 register) same clock\$3) and (processor adj array)	USPAT; US-PGPUB